## DESIGN PATENT APPLICATION IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	:	RalfLERNER	Group Art Unit 1763 Examiner William D. Coleman Confirmation No. 2111
Appln. No.	:	10/552,984	
Docket No.	:	P28504	
Customer No.	:	07055	
Filed	:	September 25, 2006	)
Title	:	TEST STRUCTURE FOR ELECTRICALLY	<u>}</u>

## COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

ETCHINGS IN AN SOI WAFER, AND ASSOCIATED WORKING METHODS

U.S. Patent and Trademark Office Customer Service Window, Mail Stop *Issue Fee* Randolph Building 401 Dulany Street Alexandria, VA 22314

Sir

This is in response to the Statement of Reasons for Allowance, set forth in an attachment to the Notice of Allowability (form PTOL-37), mailed May 4, 2009.

Although Applicant does not here express disagreement with the Examiner in his indication of reasons for allowance, Applicant notes that the allowed claims recite a plurality of features and the patentability of the allowed claims should be considered to be based upon the totality of the features recited therein, i.e., the invention should be "considered as a whole," as defining over the prior art. See, e.g., Panduit Corp. v. Dennison Mfg. Co., 810 F.2d 1561, 1 USPO2d 1593 (Fed. Cir. 1987).

For example, Applicant submits that the reasons for allowance do not preclude the existence of additional reasons that could be cited as supporting the patentability of the allowed claims, *i.e.*, independent claims as well as dependent claims.

Respectfully submitted, Ralf LERNER

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